

SF928A

Narrow Band Low Loss RF Filter for ISM Application, balanced and unbalanced operation

This product is lead-free in compliance with RoHs 2002/95/EC.

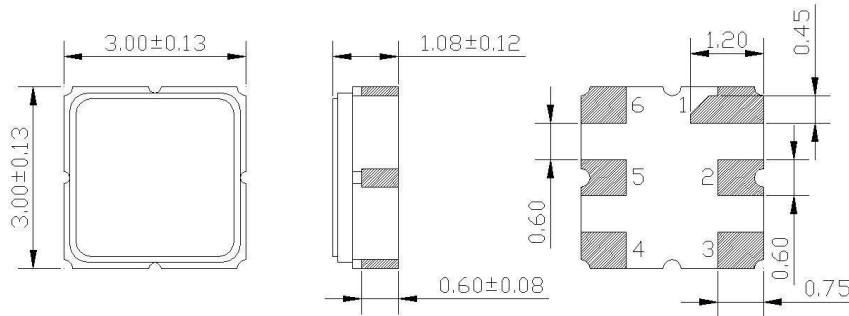
Test Conditions:

| | | |
|---|-------------|---|
| RF power | -10 dBm | |
| Temperature | 23 °C | |
| DC Voltage | 6 V | |
| Terminating source impedance (Z_S): | 50 Ω | <input checked="" type="checkbox"/> Matching Required |
| Terminating load impedance (Z_L): | 50 Ω | <input checked="" type="checkbox"/> Matching Required |

| | minimum | typical | maximum | unit |
|--|---------|------------|---------|--------------------|
| Centre frequency | | 928.35 | | MHz |
| Insertion Loss in Pass Band 928.15 MHz – 928.55 MHz | | | 3.8 | dB |
| Ripple in Pass Band 928.15 MHz – 928.55 MHz | | | 1.3 | dB |
| Rejection | | | | |
| 10 MHz – 600 MHz | 55 | | | dB |
| 600 MHz – 750 MHz | 45 | | | dB |
| 750 MHz – 918 MHz | 40 | | | dB |
| 918 MHz – 922 MHz | 32 | | | dB |
| 922 MHz – 925 MHz | 22 | | | dB |
| 925 MHz – 926 MHz | 18 | | | dB |
| 931 MHz – 932 MHz | 18 | | | dB |
| 932 MHz – 942 MHz | 28 | | | dB |
| 942 MHz – 1000 MHz | 38 | | | dB |
| 1000 MHz – 2500 MHz | 50 | | | dB |
| RF Power | | | 10 | dBm |
| Operating temperature range | -40 | | +125 | °C |
| Storage temperature range | -40 | | +125 | °C |
| Impedance for matching Z_{IN} | | TBD TBD | | Ω pF |
| Impedance for matching Z_{OUT} | | TBD TBD | | Ω pF |
| Temperature coefficient of frequency | | -0.032 | | ppm/K ² |

Electrostatic Sensitive Device

Package: S2X / 3.0*3.0mm²



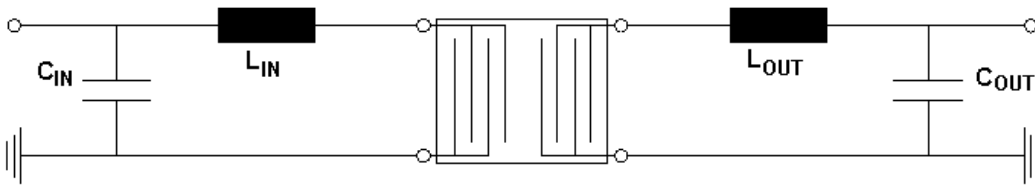
- | | | | |
|-------|-----------------------|-------|-------------------------|
| Pin 1 | Input Ground or Input | Pin 4 | Output or Output Ground |
| Pin 2 | Input or Input Ground | Pin 5 | Output Ground or Output |
| Pin 3 | To be grounded | Pin 6 | To be grounded |

All dimensions in mm

Matching network to 50 Ω: ¹⁾

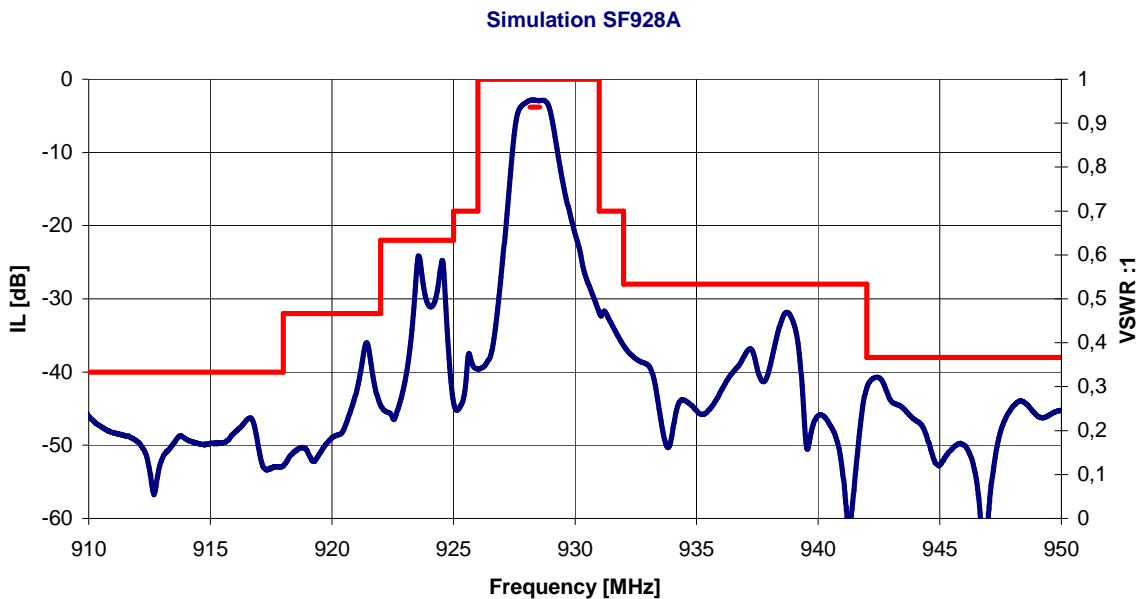
L_{IN} : TBDnH
 C_{IN} : TBDpF

L_{OUT} : TBDnH
 C_{OUT} : TBDpF



¹⁾ Matching elements are based on circuit with ideal components. Matching values may vary due to PCB layout and real components.

expected performance:



Data Sheet Target

