

## SDL868A – SAW delay line

**This product is lead-free in compliance with RoHs 2002/95/EC.**

### Test Conditions:

RF power	0 dBm	
Temperature	23 °C	
Terminating source impedance ( $Z_S$ ):	50 $\Omega$	<input checked="" type="checkbox"/> Matching Required
Terminating load impedance ( $Z_L$ ):	50 $\Omega$	<input checked="" type="checkbox"/> Matching Required

	minimum	typical	maximum	unit
Centre frequency		867.5		MHz
Insertion Loss in Pass Band <sup>1)</sup> 867 MHz – 868 MHz		7.8	9.0	dB
Amplitude Variation 867 MHz – 868 MHz		0.5	1.0	dB
Input VSWR 867 MHz – 868 MHz		2:1	2.5:1	
Output VSWR 867 MHz – 868 MHz		1.6:1	2:1	
Group delay variation 867 MHz – 868 MHz		200	300	ns
Average absolute delay 867 MHz – 868 MHz	0.95	1	1.05	$\mu$ s
Operating temperature range	-40		+125	°C
Storage temperature range	-40		+125	°C
Temperature coefficient of frequency		0.032		ppm/K <sup>2</sup>
Power			10	dBm

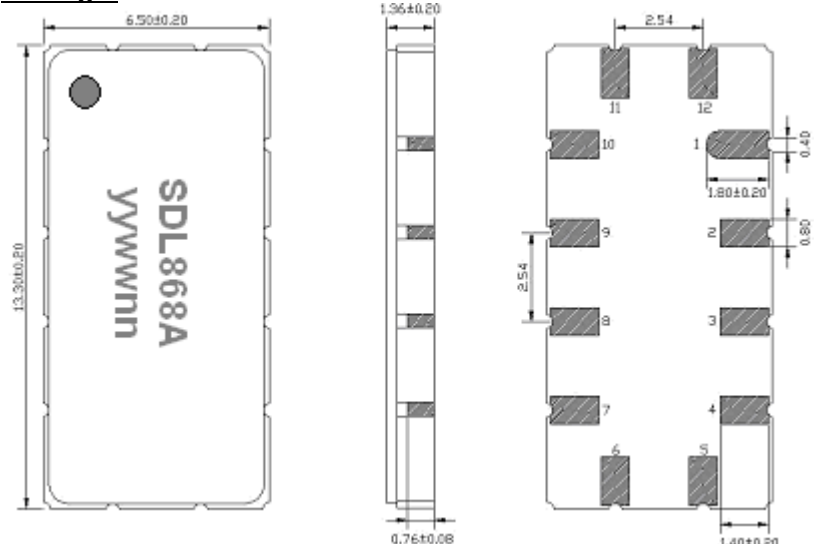
**Electrostatic Sensitive Device**

<sup>1)</sup> in case of virtual matching

### Connections:

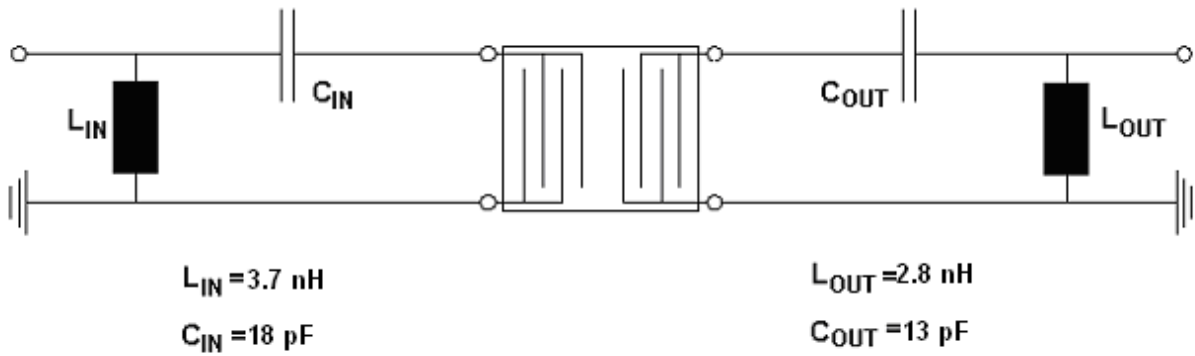
- Pin 1: Case ground
- Pin 2: Input
- Pin 3: Case ground
- Pin 4: Case ground
- Pin 5: to be grounded
- Pin 6: Output ground
- Pin 7: Case ground
- Pin 8: Output
- Pin 9: Case ground
- Pin 10: Case ground
- Pin 11: Output ground
- Pin 12: Input ground

### Package: SK1362

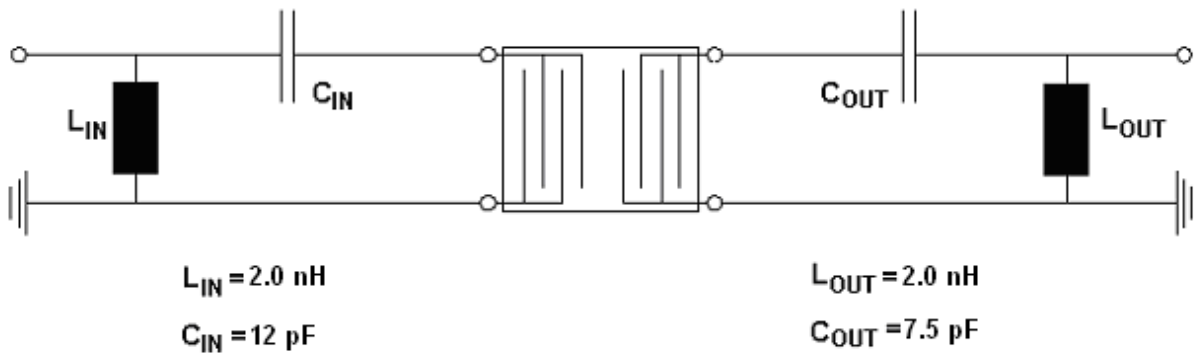


**Matching network to 50 Ω:**

Matching elements based on circuit with ideal components (virtual matching):

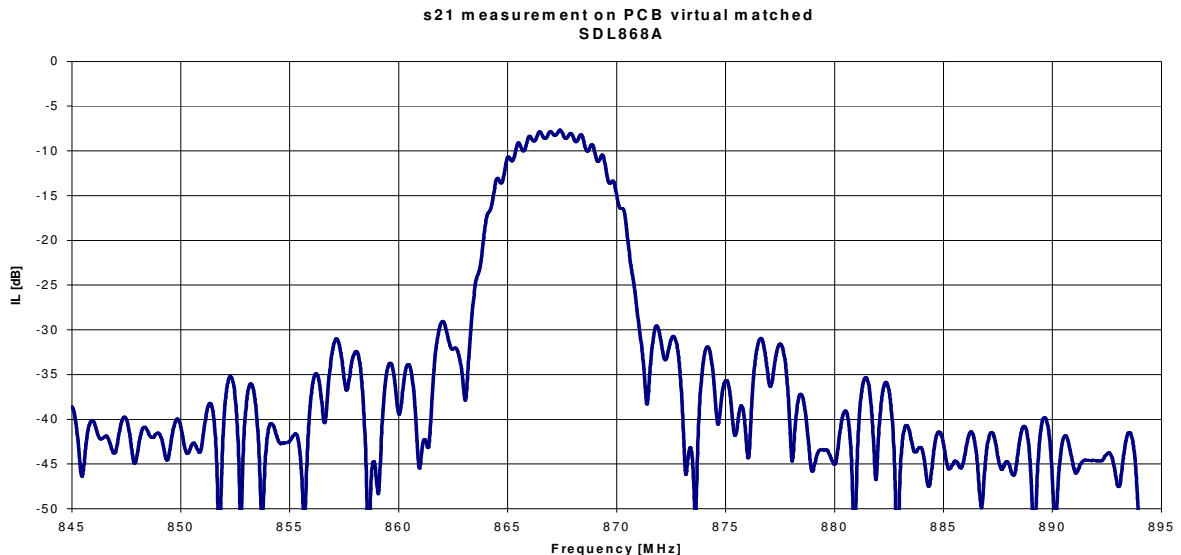


Matching elements are based on real components on PCB SDL868A-board v02:

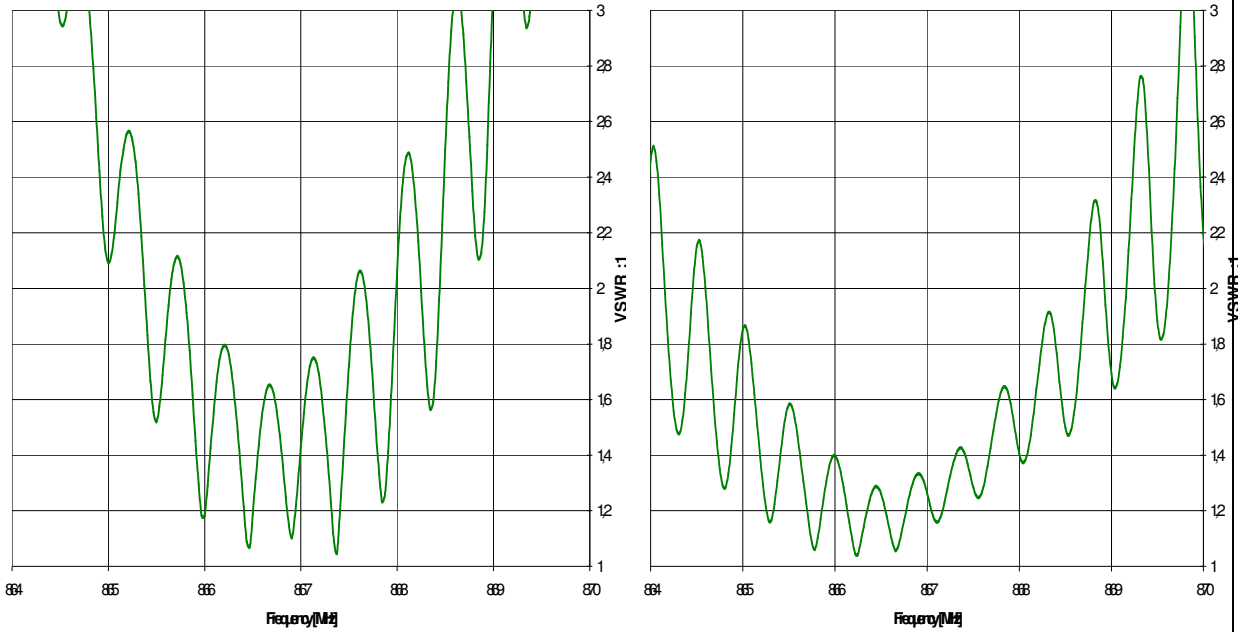


Matching values may vary due to PCB layout and real components.

**Typical performance:**



**VSWR:**



**Group delay:**

group delay measurement on PCB virtual matched  
 SDL868A

